

Extracting a Certified Static Analyser in Constructive Logic: Application to Proof Carrying Code

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CR2 INRIA - Lande project

Static program analysis

The goals of static program analysis

- ▶ To prove properties about the run-time behaviour of a program
- ▶ In a fully automatic way
- ▶ Without actually executing this program

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Solid foundations for designing an analyser

- ▶ Abstract Interpretation gives a guideline
 - ▶ to formalise analyses
 - ▶ to prove their soundness with respect to the semantics of the programming language
- ▶ Resolution of constraints on lattices by iteration and symbolic computation

So what's the problem ?

Proof

$$\begin{aligned}
 & \hat{\alpha}[P](\text{Post}[\text{if } B \text{ then } S; \text{ else } S_f \text{ fi}]) \\
 = & \quad \{\text{def. (110) of } \hat{\alpha}[P]\} \\
 & \hat{\alpha}[P] \circ \text{Post}[\text{if } B \text{ then } S; \text{ else } S_f \text{ fi}] \circ \tilde{y}[P] \\
 = & \quad \{\text{def. (103) of Post}\} \\
 & \hat{\alpha}[P] \circ \text{post}[\tau^*[\text{if } B \text{ then } S; \text{ else } S_f \text{ fi}]] \circ \tilde{y}[P] \\
 = & \quad \{\text{big step operational semantics (93)}\} \\
 & \hat{\alpha}[P] \circ \text{post}[(1_{\Sigma[P]} \cup \tau^B) \circ \tau^*[S_f] \circ (1_{\Sigma[P]} \cup \tau^I) \cup (1_{\Sigma[P]} \cup \tau^B) \circ \tau^*[S_f] \circ (1_{\Sigma[P]} \cup \tau^I) \circ \tilde{y}[P]] \\
 = & \quad \{\text{Galois connection (98) so that post preserves joins}\} \\
 & \hat{\alpha}[P] \circ (\text{post}[(1_{\Sigma[P]} \cup \tau^B) \circ \tau^*[S_f] \circ (1_{\Sigma[P]} \cup \tau^I)] \cup \text{post}[(1_{\Sigma[P]} \cup \tau^B) \circ \tau^*[S_f] \circ (1_{\Sigma[P]} \cup \tau^I)]) \circ \tilde{y}[P] \\
 = & \quad \{\text{Galois connection (106) so that } \hat{\alpha}[P] \text{ preserves joins}\} \\
 & (\hat{\alpha}[P] \circ \text{post}[(1_{\Sigma[P]} \cup \tau^B) \circ \tau^*[S_f] \circ (1_{\Sigma[P]} \cup \tau^I)] \circ \tilde{y}[P]) \hat{\cup} (\hat{\alpha}[P] \circ \text{post}[(1_{\Sigma[P]} \cup \tau^B) \circ \tau^*[S_f] \circ (1_{\Sigma[P]} \cup \tau^I)] \circ \tilde{y}[P]) \\
 \stackrel{\text{E}}{=} & \quad \{\text{lemma (5.3) and similar one for the else branch}\} \\
 \lambda J \cdot \text{let } J^I = \lambda l \in \text{in}_P[P] \cdot (l = \text{at}_P[S_f] ? J_{\text{at}_P[S_f]} \hat{\cup} \text{Abexp}[B](J_l) \hat{\cup} J_l) \text{ in} & \quad (120) \\
 \quad \text{let } J^{I'} = \text{APost}[S_f](J^{I'}) \text{ in} \\
 \quad \quad \lambda l \in \text{in}_P[P] \cdot (l = l' ? J_{l'}^{I'} \hat{\cup} J_{\text{after}_P[S_f]}^{I'} \hat{\cup} J_l^{I'}) \\
 \hat{\cup} \\
 \quad \text{let } J^I = \lambda l \in \text{in}_P[P] \cdot (l = \text{at}_P[S_f] ? J_{\text{at}_P[S_f]} \hat{\cup} \text{Abexp}[T(\neg B)](J_l) \hat{\cup} J_l) \text{ in} \\
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 = & \quad \{\text{by grouping similar terms}\} \\
 \lambda J \cdot \text{let } J^{I'} = \lambda l \in \text{in}_P[P] \cdot (l = \text{at}_P[S_f] ? J_{\text{at}_P[S_f]} \hat{\cup} \text{Abexp}[B](J_l) \hat{\cup} J_l) & \\
 \text{and } J^I = \lambda l \in \text{in}_P[P] \cdot (l = \text{at}_P[S_f] ? J_{\text{at}_P[S_f]} \hat{\cup} \text{Abexp}[T(\neg B)](J_l) \hat{\cup} J_l) \text{ in} & \\
 \quad \text{let } J^{I''} = \text{APost}[S_f](J^{I'}) & \\
 \quad \text{and } J^{I'''} = \text{APost}[S_f](J^{I'}) \text{ in} & \\
 \quad \quad \lambda l \in \text{in}_P[P] \cdot (l = l' ? J_{l'}^{I''} \hat{\cup} J_{\text{after}_P[S_f]}^{I''} \hat{\cup} J_l^{I''} \hat{\cup} J_{\text{after}_P[S_f]}^{I'''} \hat{\cup} J_l^{I'''} \hat{\cup} J_l^{I''}) & \\
 = & \quad \{\text{by locality (113) and labelling scheme (59) so that in particular } J_{l'}^{I''} = J_{l'}^{I'''} = J_{l'}^{I''} = J_{l'}^{I'''} \\
 & = J_{l'}^{I''} = J_{l'}^{I'''} \text{ and } \text{APost}[S_f] \text{ and } \text{APost}[S_f] \text{ do not interfere}\}
 \end{aligned}$$

Proof

$$\begin{aligned}
 & \hat{\alpha}[P](\text{Post}[\text{if } B \text{ then } S; \text{ else } S_f \text{ fi}]) \\
 = & \quad \{\text{def. (110) of } \hat{\alpha}[P]\} \\
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 = & \quad \{\text{def. (103) of Post}\} \\
 & \hat{\alpha}[P] \circ \text{post}[\tau^*[\text{if } B \text{ then } S; \text{ else } S_f \text{ fi}]] \circ \tilde{y}[P] \\
 = & \quad \{\text{big step operational semantics (93)}\} \\
 & \hat{\alpha}[P] \circ \text{post}[(1_{\Sigma[P]} \cup \tau^B) \circ \tau^*[S] \circ (1_{\Sigma[P]} \cup \tau^f) \cup (1_{\Sigma[P]} \cup \tau^{\bar{B}}) \circ \tau^*[S_f] \circ (1_{\Sigma[P]} \cup \tau^f) \cup \tau^f] \circ \tilde{y}[P] \\
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 \stackrel{\text{E}}{=} & \quad \{\text{lemma (5.3) and similar one for the else branch}\} \\
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 \quad \text{let } J^{f'} = \text{APost}[S_f](J^{f'}) \text{ in} & \\
 \quad \lambda l \in \text{in}_P[P] \cdot (l = l' ? J_{l'}^{f'} \hat{\cup} J_{\text{after}_P[S_f]}^{f'} \hat{\cup} J_l^{f'}) & \\
 \hat{\cup} & \\
 \quad \text{let } J^f = \lambda l \in \text{in}_P[P] \cdot (l = \text{at}_P[S] ? J_{\text{at}_P[S]} \hat{\cup} \text{Abexp}[T(\neg B)](J_l) \hat{\cup} J_l) \text{ in} & \\
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 = & \quad \{\text{by locality (113) and labelling scheme (59) so that in particular } J_{l'}^{f''} = J_{l'}^{f'''} = J_{l'}^{f''} = J_{l'}^{f'''} \\
 = J_{l'}^{f''} = J_{l'}^{f'''} \text{ and APost}[S_f] \text{ and APost}[S_f] \text{ do not interfere}\} &
 \end{aligned}$$

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Implementation

```

matrix_t* _matrix_alloc_int(const int mr, const int nc)
{
    matrix_t* mat = (matrix_t*)malloc(sizeof(matrix_t));
    mat->nbrrows = mat->maxrows - mr;
    mat->nbcolumns = nc;
    mat->sorted = s;
    if (mr+nc>0) {
        int i;
        pkint_t* q;
        mat->_pinit = _vector_alloc_int(mr+nc);
        mat->p = (pkint_t**)malloc(mr * sizeof(pkint_t*));
        q = mat->_pinit;
        for (i=0; i<mr; i++) {
            mat->p[i]=q;
            q=q+nc;
        }
    }
    return mat;
}

void backsubstitute(matrix_t* con, int rank)
{
    int i, j, k;
    for (k=rank-1; k>=0; k--) {
        j = pk_choleski_intp[k];
        for (i=0; i<k; i++) {
            if (pkint_sgn(con->p[i][j]))
                matrix_combine_rows(con, i, k, i, j);
        }
        for (i=k+1; i<con->nbrrows; i++) {
            if (pkint_sgn(con->p[i][j]))
                matrix_combine_rows(con, i, k, i, j);
        }
    }
}
    
```

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Proof

$$\begin{aligned}
 & \hat{\alpha}[P] \text{Post}[\text{if } B \text{ then } S; \text{ else } S_f \text{ fi}] \\
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 \stackrel{\text{iii}}{=} & \text{\textasciitilde{lemma}} \\
 \lambda J \cdot \text{let } & \text{let } \\
 & \lambda l \in \text{in}_P[P]. (l = l' ? J_{l'}^{f''} \hat{\cup} J_{\text{after}_P[S]}^{f''} \hat{\cup} J_l^{f''}) \\
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 = & \text{\textasciitilde{by locality (113) and labelling scheme (S9) so that in particular } } J_{l'}^{f''} = J_{l'}^f = J_{l'}^e = J_{l'}^f \\
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        }
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}

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        }
        for (i=k+1; i<con->nbrrows; i++) {
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        }
    }
}

```

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Do the two parts talk about the same?

Certified static analyses

A *certified static analysis* is an analysis whose implementation has been formally proved correct using a proof assistant.



- ▶ proof assistant : Coq
 - ▶ we benefit from the extraction mechanism to prove executable analyser
- ▶ proof technique : abstract interpretation
 - ▶ general enough to handle a broad range of static analysis
- ▶ applications to static analysis of bytecode programs
 - ▶ to go beyond the state of the art about Sun's bytecode verifier

Contributions

- ▶ We have isolated a fragment of Abstract Interpretation adequate for proving analysis soundness in the constructive logic of Coq
- ▶ We have programmed certified generic fixpoint solvers
- ▶ We have developed a lattice library allowing an easy construction of complex terminations proofs
- ▶ Several cases studies for bytecode Java
 - ① A control flow analysis for a representative subset of JavaCard
 - ② A memory usage analysis for a representative subset of JavaCard
 - ③ An interval analysis for an imperative fragment of JavaCard with dynamic arrays (see PCC applications)
- ▶ Application to proof carrying code

Outline

1 Introduction

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- 2 Building a certified static analyser

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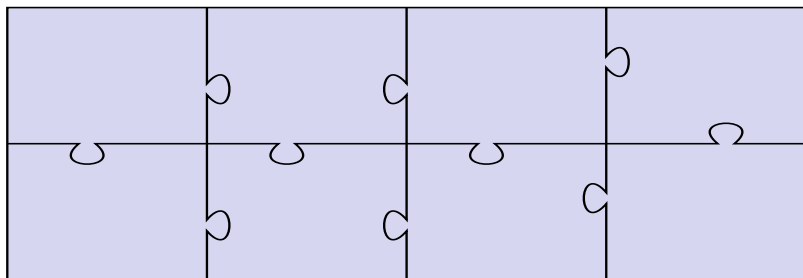
Outline

- 1 Introduction
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- 4 Perspectives

Outline

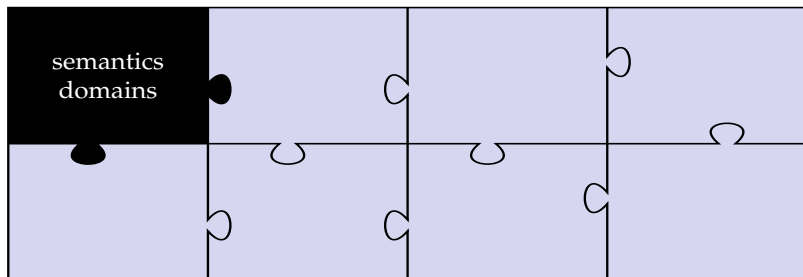
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Building a certified static analyser

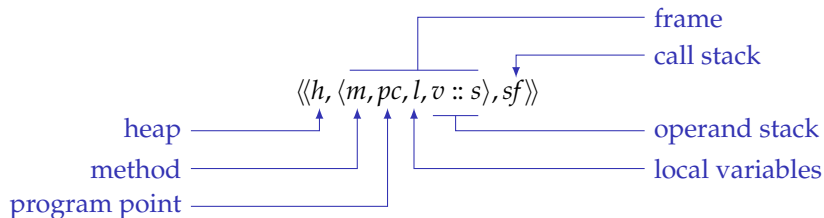


- ▶ A puzzle with 8 pieces,
- ▶ Each piece interacts with its neighbors

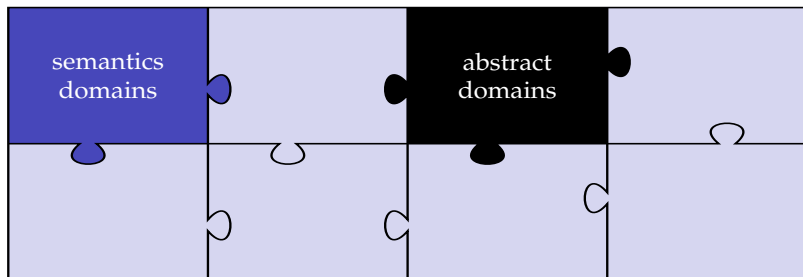
Building a certified static analyser



Example: JVM states

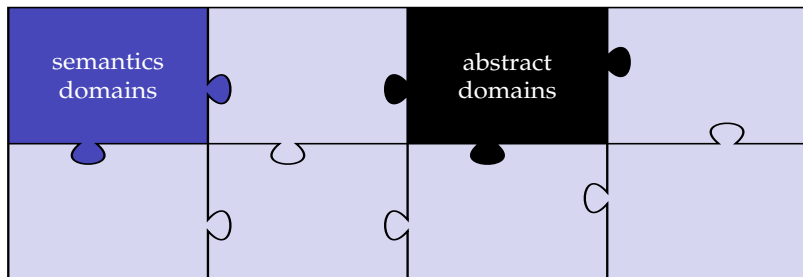


Building a certified static analyser



- ▶ Each semantic sub-domain has its abstract counterpart
- ▶ An abstract domain is a lattice $(\mathcal{D}^\#, =, \sqsubseteq, \perp, \sqcup, \sqcap)$ without infinite strictly increasing chains $x_0 \sqsubset x_1 \sqsubset \dots \sqsubset \dots$
- ▶ First difficult point: how can we quickly develop big lattice structures in Coq ?

Building a certified static analyser



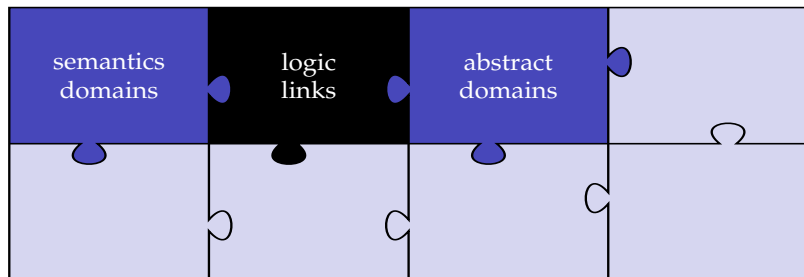
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- ▶ First difficult point: how can we quickly develop big lattice structures in Coq?
 - ▶ generic lattice library

Building lattices in Coq

We propose a technique based on the Coq module system (inspired by the ML module system)

- ▶ Lattice requirements are collected in a module contract
- ▶ Various functors are proposed in order to build lattices by composition of others
 - ▶ Base lattices: signs, congruences, constants, intervals, finite set (implemented with trees)
 - ▶ Functors : product, disjoint and lifted sums, lists, functional arrays (implemented with trees)
 - ▶ For each functor the most challenging proofs deals with the preservation of the termination criterion.
- ▶ The library deals as well with widening/narrowing

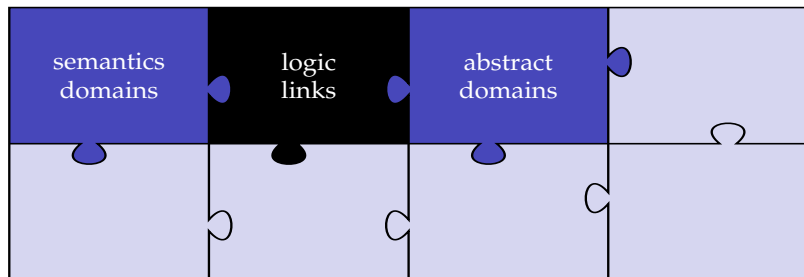
Building a certified static analyser



- ▶ Each abstract value represents a property on concrete values
- ▶ This correspondence is formalised by a monotone concretisation function

$$\gamma : (\mathcal{D}^\#, \sqsubseteq) \longrightarrow_m (\wp(\mathcal{D}), \subseteq)$$

Building a certified static analyser

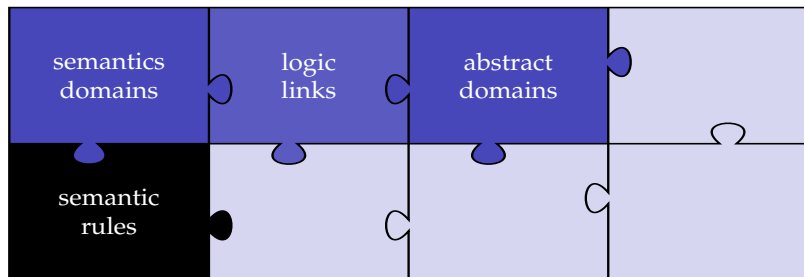


- ▶ Each abstract value represents a property on concrete values
- ▶ This correspondence is formalised by a monotone concretisation function

$$\gamma : (\mathcal{D}^\#, \sqsubseteq) \longrightarrow_m (\wp(\mathcal{D}), \subseteq)$$

$x \subseteq \gamma(x^\#)$ means “ $x^\#$ is a correct approximation of x ”

Building a certified static analyser



- ▶ operational semantics $\cdot \rightarrow_p \cdot$ between states
- ▶ collecting semantics: $\llbracket P \rrbracket = \{ s \mid \exists s_0 \in S_{\text{init}}, s_0 \rightarrow_p^* s \}$
- ▶ we want to compute a correct approximation of $\llbracket P \rrbracket$
 - ▶ a sound invariant s^\sharp on the reachable states : $\llbracket P \rrbracket \subseteq \gamma(s^\sharp)$

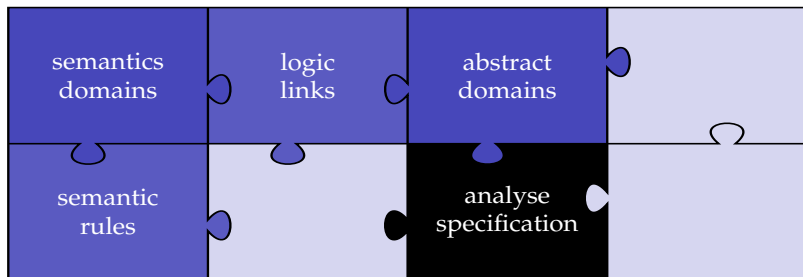
Example : JVM operational semantics

$$\frac{\text{instructionAt}_p(m, pc) = \text{push } c}{\langle\langle h, \langle m, pc, l, s \rangle, sf \rangle\rangle \rightarrow \langle\langle h, \langle m, pc + 1, l, c :: s \rangle, sf \rangle\rangle}$$

$$\begin{aligned} \text{instructionAt}_p(m, pc) &= \text{invokevirtual } m_{id} \\ m' &= \text{methodLookup}(m_{id}, h(loc)) \\ V &= v_1 :: \dots :: v_{\text{nbArguments}(m_{id})} \end{aligned}$$

$$\langle\langle h, \langle m, pc, l, loc :: V :: s \rangle, sf \rangle\rangle \rightarrow \langle\langle h, \langle m', 1, V, \epsilon \rangle, \langle m, pc, l, s \rangle :: sf \rangle\rangle$$

Building a certified static analyser



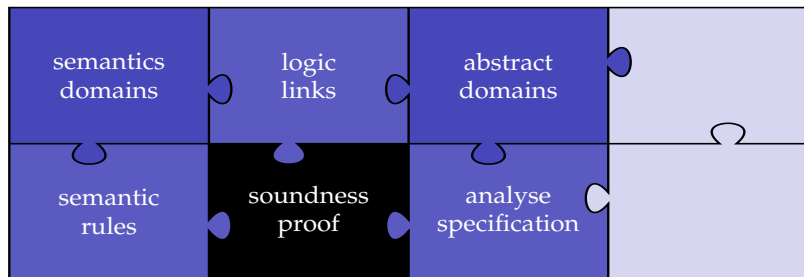
- ▶ the analysis is specified as a solution of a post fixpoint problem

$$F_p^\#(s^\#) \sqsubseteq^\# s^\#$$

- ▶ after partitioning : constraint system

$$\begin{cases} f_1^\#(s_1^\#, \dots, s_n^\#) \sqsubseteq^\# s_{i_1}^\# \\ \dots \\ f_n^\#(s_1^\#, \dots, s_n^\#) \sqsubseteq^\# s_{i_n}^\# \end{cases}$$

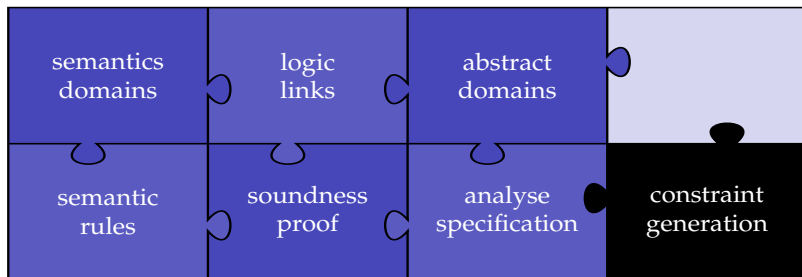
Building a certified static analyser



$$\forall P, \forall s^\sharp, \quad F_P^\sharp(s^\sharp) \sqsubseteq^\sharp s^\sharp \Rightarrow \llbracket P \rrbracket \subseteq \gamma(s^\sharp)$$

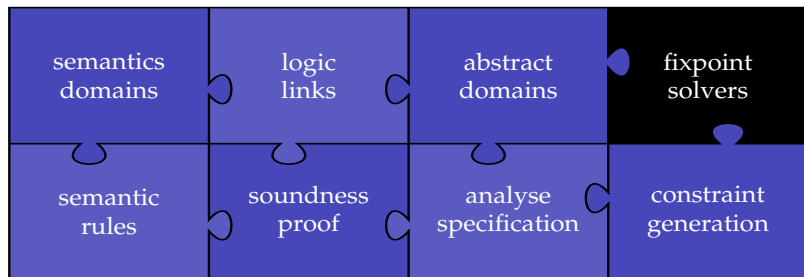
- ▶ easy proof, but tedious
- ▶ one proof by instruction: a long work for real languages

Building a certified static analyser



- ▶ collects all constraints in a program
- ▶ generic tool

Building a certified static analyser



$$\forall P, \exists s^\#, F_P^\#(s^\#) \sqsubseteq s^\#$$

Two techniques of iterative computation

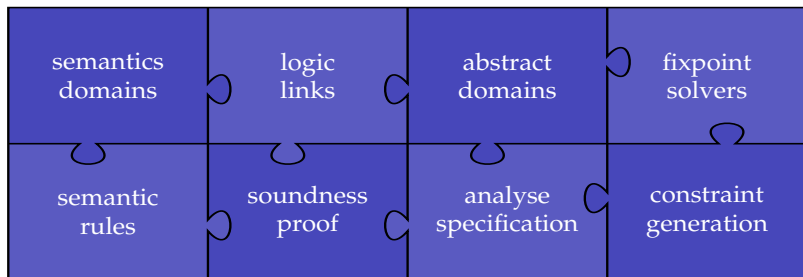
- ▶ traditional least (post)-fixpoint computation

$$\perp \rightarrow F_P^\#(\perp) \rightarrow F_P^{\#2}(\perp) \rightarrow \dots \text{lfp}(F_P^\#)$$

- ▶ post-fixpoint computation by widening/narrowing with chaotic iterations

In the two cases, a generic tool

Building a certified static analyser



Final result

$$\left. \begin{array}{l} \forall P, \forall s^\#, F_P^\#(s^\#) \sqsubseteq s^\# \Rightarrow \llbracket P \rrbracket \subseteq \gamma(s^\#) \\ \forall P, \exists s^\#, F_P^\#(s^\#) \sqsubseteq s^\# \end{array} \right\} \forall P, \exists s^\#, \llbracket P \rrbracket \subseteq \gamma(s^\#)$$

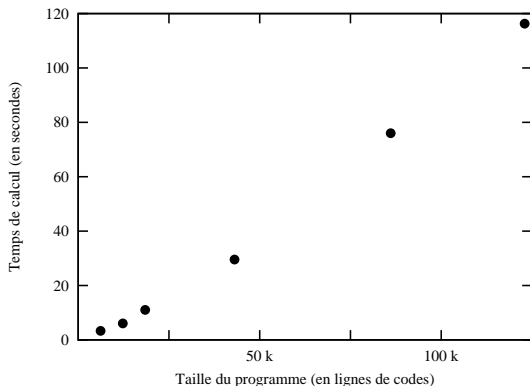
In Coq : `analyse : $\forall p:\text{program}, \{ s:\text{abstate} \mid \text{sem}(P) \subseteq \text{gamma}(P, s) \}$`

In Caml : `analyse : program \rightarrow abstate`

Efficiency of the extracted program

Experiments on the memory usage analysis

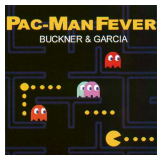
$$\mathcal{D}^\sharp = (\mathbb{M} \rightarrow \mathcal{P}(\mathbb{M})) \times \mathcal{P}(\mathbb{M}) \times (\mathbb{M} \rightarrow \mathbb{P} \rightarrow \mathcal{P}(\mathbb{P})) \times \mathcal{P}(\mathbb{M})$$



Outline

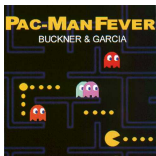
- 1 Introduction
- 2 Building a certified static analyser
- 3 Application to Proof Carrying Code**
- 4 Perspectives

Mobile code dilemmas...



Mobile code dilemmas...

Untrusted code



Safe ?

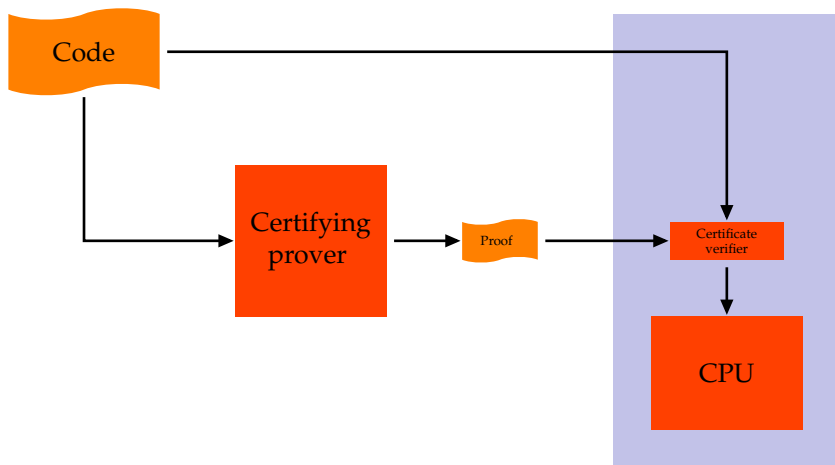


Host system

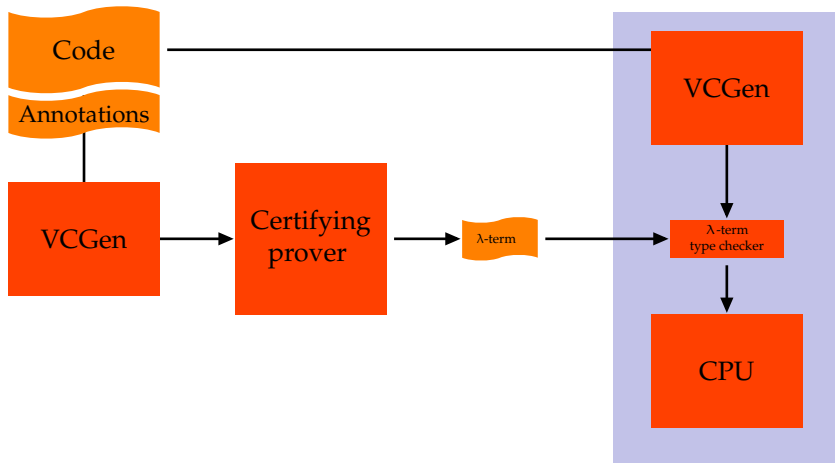


- ▶ The untrusted code may cause damages on the system
 - ▶ intern structure corruption
- ▶ The untrusted code may use too many resources
 - ▶ CPU, memory, SMS...

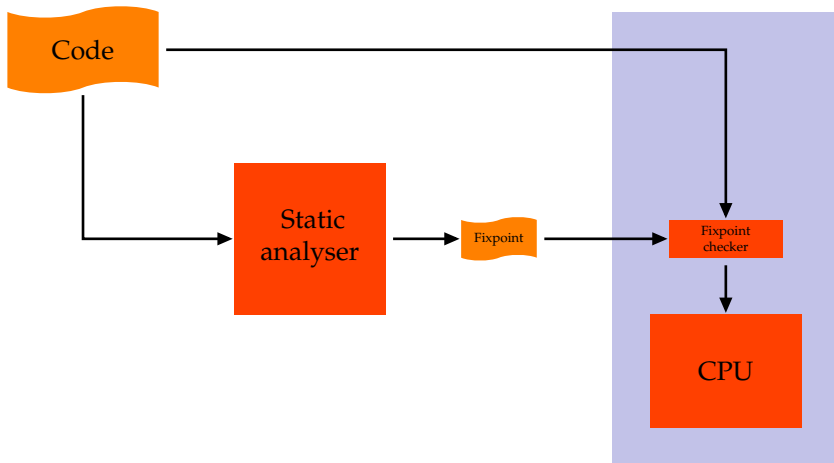
Proof carrying code



Proof carrying code: standard framework



Proof carrying code by abstract interpretation



Proof carrying code by abstract interpretation

PCC requirements:

- ▶ the certificate must be small
- ▶ the verifier must be efficient
- ▶ soundness of the certifying prover is not critical
- ▶ soundness of the verifier is critical

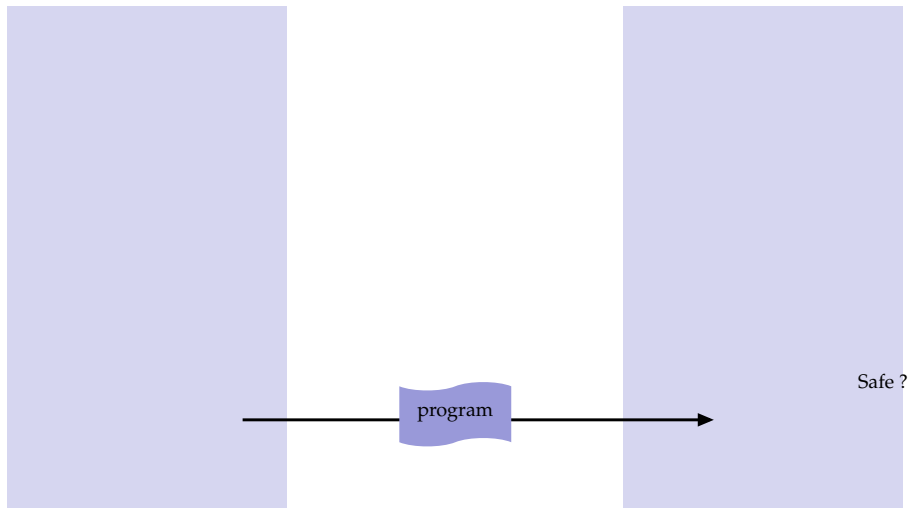
Our works on certified static analysis allows the consumer to check semantics soundness of the verifier

- ▶ certified proof carrying code = certified static analysis without fixpoint solver

Proof carrying code by certified abstract interpretation

Producer

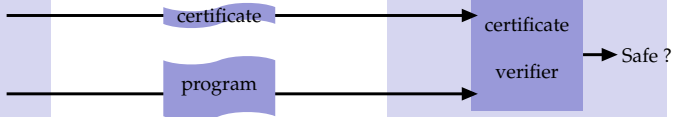
Consumer



Proof carrying code by certified abstract interpretation

Producer

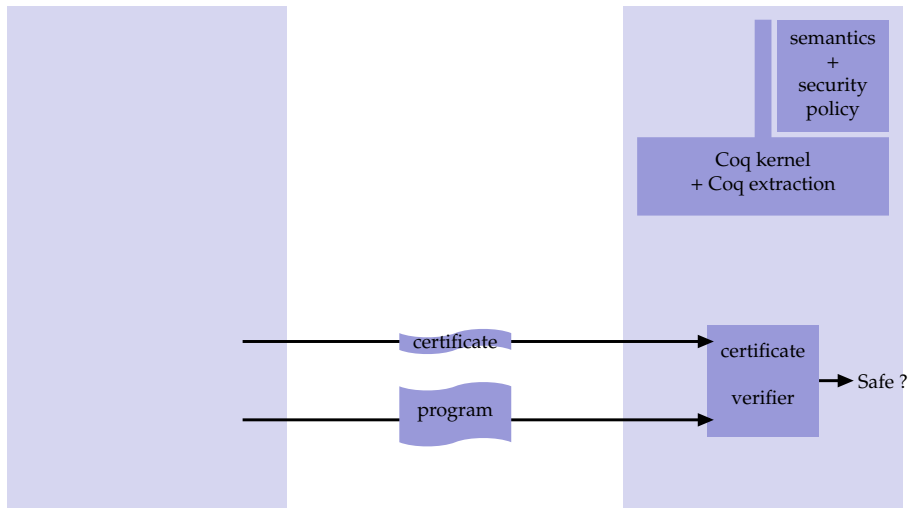
Consumer



Proof carrying code by certified abstract interpretation

Producer

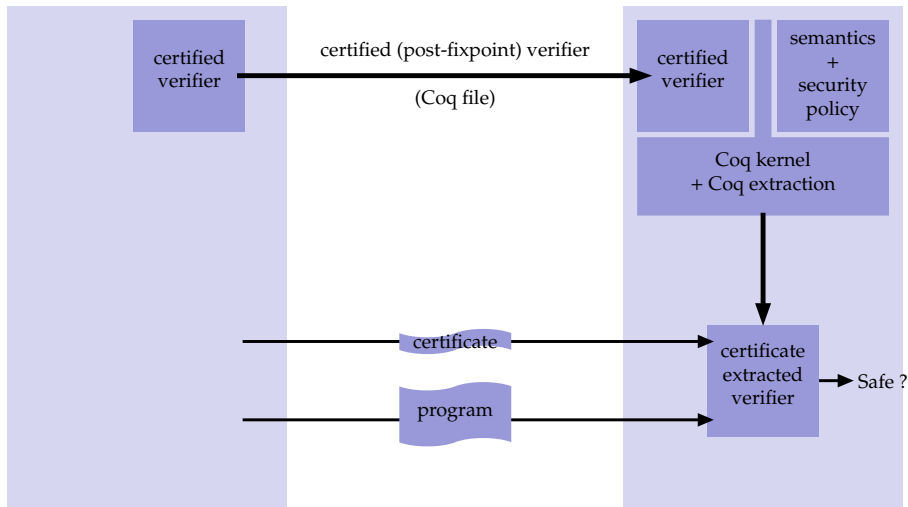
Consumer



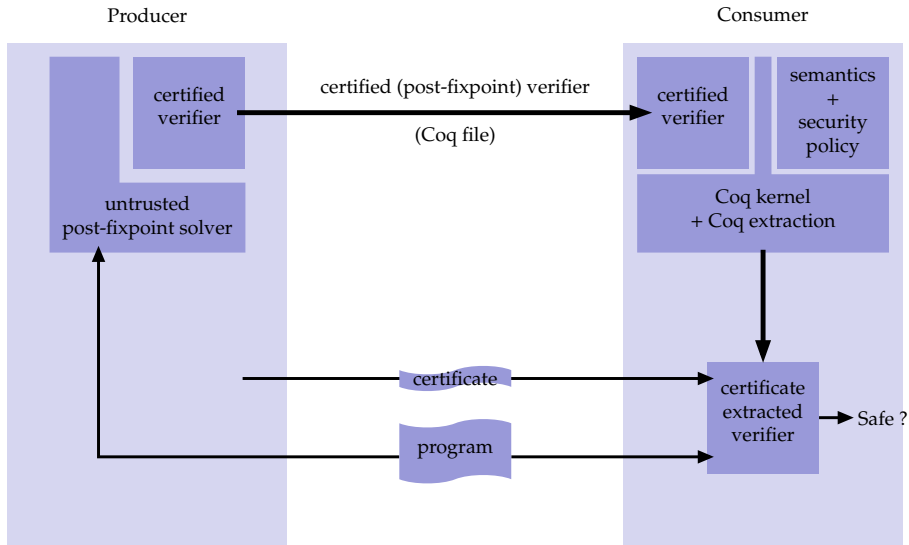
Proof carrying code by certified abstract interpretation

Producer

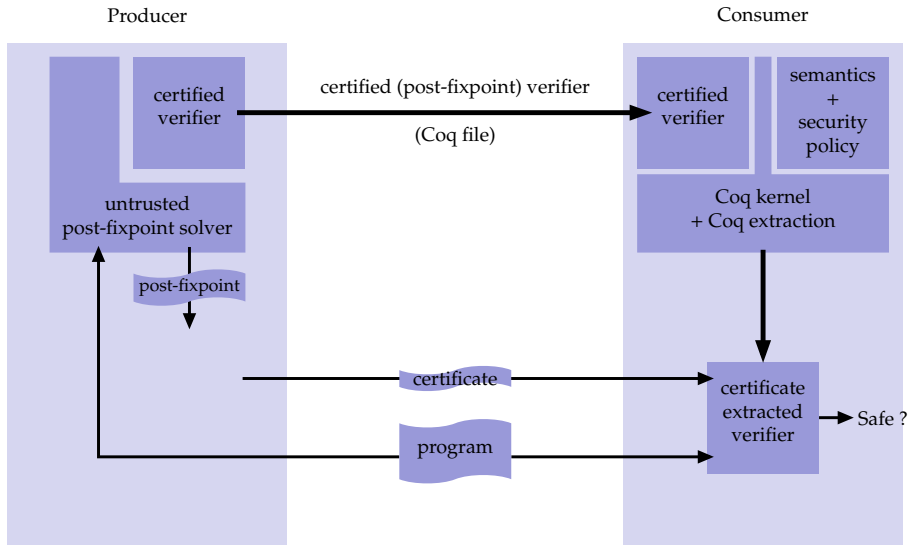
Consumer



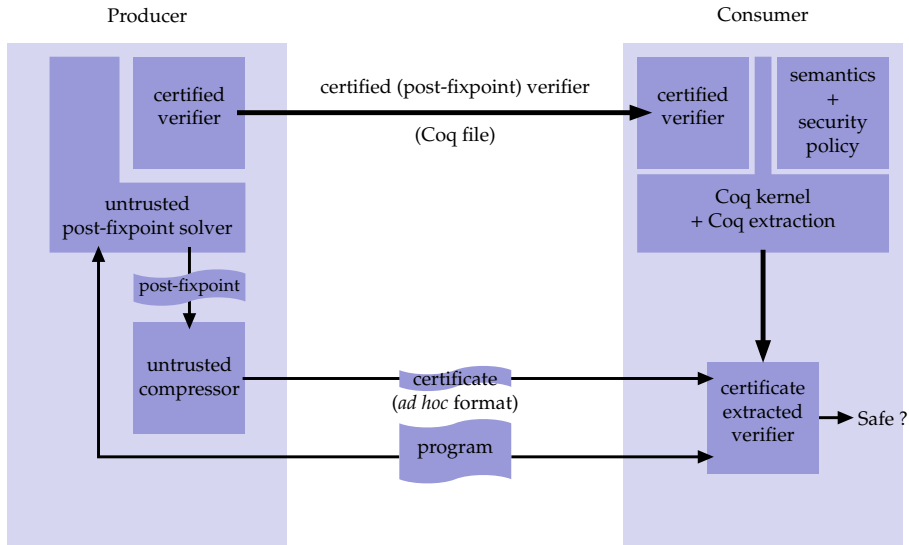
Proof carrying code by certified abstract interpretation



Proof carrying code by certified abstract interpretation



Proof carrying code by certified abstract interpretation



First case study

- ▶ imperative fragment of bytecode Java + dynamically allocated arrays
- ▶ we verify arrays access (no out of bounds errors) by intervals approximation
- ▶ experiments on various algorithms¹:

Program	.java size	.class size	certificate size	checking time
BubbleSort	440	528	32	0.015
HeapSort	1044	858	63	0.050
QuickSort	1078	965	124	0.060
ConvolutionProduct	378	542	52	0.010
FloydWharshall	417	596	134	0.020
PolynomProduct	509	604	87	0.010

certificate/programme ~ 10%, verification time < 0.1s

¹Size file in bytes, time in secs

Outline

- 1 Introduction
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Perspectives

Increasing the power of certified static analyses

- ▶ symbolic manipulation
- ▶ relational abstract domains (octagons, polyhedra)

Proof-carrying code

- ▶ fixpoint compression (see ESOP'07)
- ▶ we want to demonstrate the scalability of our architecture

Pre-analyses for others verification tools

- ▶ automatically detect some unthrowable exceptions
- ▶ alias analysis
- ▶ race detection